

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Andrew Dellow et al.  
Application No. : 10/583,577  
Confirmation No. : 1336  
Filed : June 19, 2006  
For : MONOLITHIC SEMICONDUCTOR INTEGRATED CIRCUIT  
AND METHOD FOR SELECTIVE MEMORY ENCRYPTION  
AND DECRYPTION

Docket No. : 851963.425USPC  
Date : July 2, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

FEE DEFICIENCY AUTHORIZATION FORM

Commissioner for Patents:

Applicants hereby authorize the Director to charge any deficiencies in fees due by way of the enclosed papers only under 37 CFR 1.16 and 1.17 to Deposit Account No. 19-1090.

Respectfully submitted,  
SEED Intellectual Property Law Group PLLC

/Robert Iannucci/  
Robert Iannucci  
Registration No. 33,514

701 Fifth Avenue, Suite 5400  
Seattle, Washington 98104  
Phone: (206) 622-4900  
Fax: (206) 682-6031

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